



6N135, 6N136, MPC4502, MPC4503 Series

DIP8, 1Mbit/s High Speed Transistor Photo Coupler

Features

- High isolation 5000 VRMS
- DC input with transistor output
- Operating temperature range - 55 °C to 100 °C
- RoHS & REACH Compliance
- Halogen free
- MSL class 1
- Regulatory Approvals
 - UL - UL1577
 - VDE - EN60747-5-5(VDE0884-5)
 - CQC – GB4943.1-2022

Applications

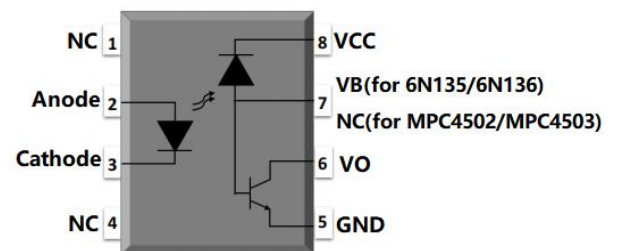
- Line receiver
- Telecommunication equipment
- Out interface to CMOS-LSTTL-TTL
- Wide bandwidth analog coupling
- Pulse transformer replacement
- Computer-peripheral interface

Description

The 6N135, 6N136, MPC450X series combine an AlGaAs infrared emitting diode as the emitter which is optically coupled to a silicon high speed photo transistor in a plastic DIP8 package with different lead forming options.

A separate design between photodiode and transistor reduces the base-collector capacitance of the input transistor which improves the speed by several orders of magnitude over conventional phototransistor optocouplers.

Schematic





6N135, 6N136, MPC4502, MPC4503 Series

DIP8, 1Mbit/s High Speed Transistor Photo Coupler

ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	VALUE	UNIT	Note
INPUT				
Forward Current	I_F	25	mA	
Peak Forward Current	I_{FP}	50	mA	1
Peak Transient Current	$I_{F(trans)}$	1	A	2
Reverse Voltage	V_R	5	V	
Input Power Dissipation	P_I	100	mW	
OUTPUT				
Supply Voltage	V_{CC}	-0.5~30	V	
Output Voltage	V_O	-0.5~20	V	
Output Current	I_O	50	mA	
Emitter-Base Reverse Voltage	V_{EBR}	5	V	
Base Current	I_B	5	mA	
Output Power Dissipation	P_O	100	mW	
COMMON				
Total Power Dissipation	P_{tot}	200	mW	
Isolation Voltage	V_{iso}	5000	V _{rms}	3
Operating Temperature	T_{opr}	-55~100	°C	
Storage Temperature	T_{stg}	-55~150	°C	
Soldering Temperature	T_{sol}	260	°C	4

Note 1. 50% duty, 1ms P.W

Note 2. $\leq 1\mu s$ P.W, 300pps

Note 3. AC For 1 Minute, R.H. = 40 ~ 60%

Note 4. For 10 seconds



6N135, 6N136, MPC4502, MPC4503 Series

DIP8, 1Mbit/s High Speed Transistor Photo Coupler

ELECTRICAL OPTICAL CHARACTERISTICS at Ta=25°C							
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITION	NOTE
INPUT(at Ta=0 to 70°C , unless specified otherwise)							
Forward Voltage	V _F	-	1.45	1.8	V	I _F =16mA	
Reverse Current	I _R	-	-	10	μA	V _R =5V	
Input Capacitance	C _{in}	-	60	-	pF	V=0, f=1MHz	
OUTPUT(at Ta=0 to 70°C , unless specified otherwise)							
High Level Supply Current	I _{CCH}	-	0.01	1	μA	I _F =0mA, V _O =Open, V _{CC} =15V, Ta=25°C	
		-	-	2	μA	I _F =0mA, V _O =Open, V _{CC} =15V	
Low Level Supply Voltage	I _{CCL}	-	200	-	μA	I _F =16mA, V _O =Open, V _{CC} =15V	
Logic High Output Current	I _{OH}	-	0.001	0.5	μA	I _F =0mA, V _O =V _{CC} =5.5V, Ta=25°C	
		-	0.01	1	μA	I _F =0mA, V _O =V _{CC} =15V, Ta=25°C	
		-	-	50	μA	I _F =0mA, V _O =V _{CC} =15V,	



6N135,6N136,MPC4502,MPC4503 Series

DIP8, 1Mbit/s High Speed Transistor Photo Coupler

ELECTRICAL OPTICAL CHARACTERISTICS

PARAMETER	SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION	NOTE
TRANSFER CHARACTERISTICS(at Ta=0 to 70°C , unless specified otherwise)							
Current Transfer Ratio	6N135	7	-	50	μA	$I_F = 16mA, V_O = 0.4V,$ $V_{CC}=4.5V, T_a=25^\circ C$	
	6N136 MPC4502 MPC4503	19	-	50			
	6N135	5	-	-		$I_F = 16mA, V_O = 0.5V,$ $V_{CC}=4.5V$	
	6N136 MPC4502 MPC4503	15	-	-			
Logic Low Output Voltage	6N135	-	0.18	0.4	μA	$I_F = 16mA, I_O = 1.1mA,$ $V_{CC}=4.5V, T_a=25^\circ C$	
	6N136 MPC4502 MPC4503	-	0.25	0.4		$I_F = 16mA, I_O = 3mA,$ $V_{CC}=4.5V, T_a=25^\circ C$	
	6N135	-	-	0.5		$I_F = 16mA, I_O = 0.8mA,$ $V_{CC}=4.5V$	
	6N136 MPC4502 MPC4503	-	-	0.5		$I_F = 16mA, I_O = 2.4mA,$ $V_{CC}=4.5V$	
Isolation Resistance	Riso	10^{12}	10^{14}	-	Ω	DC500V, 40 ~ 60% R.H.	
Floating Capacitance	C _{IO}	-	0.3	1	pF	V=0, f=1MHz	

ELECTRICAL OPTICAL CHARACTERISTICS

PARAMETER		SYMBOL	MIN	TYP	MAX	UNIT	TEST CONDITION	NOTE
SWITCHING CHARACTERISTICS(at Ta=0 to 70°C, I _F =16mA, V _{CC} =5V, unless specified otherwise)								
Propagation Delay Time to Logic Low	6N135	TPHL	-	0.35	1.5	μs	R _L =4.1kΩ, T _A =25°C	Fig.13
			-	-	2		R _L =4.1kΩ	
	6N136		-	0.35	0.8		R _L =1.9kΩ, T _A =25°C	
	MPC4502 MPC4503		-	-	1.0		R _L =1.9kΩ	
Propagation Delay Time to Logic High	6N135	TPLH	-	0.5	1.5	μs	R _L =4.1kΩ, T _A =25°C	Fig.13
			-	-	2		R _L =4.1kΩ	
	6N136		-	0.3	0.8		R _L =1.9kΩ, T _A =25°C	
	MPC4502 MPC4503		-	-	1.0		R _L =1.9kΩ	
Common Mode Transient Immunity at Logic High	6N135	CM _H	1000	-	-	V/μs	I _F = 0mA, V _{CM} =10Vpp, R _L =4.1kΩ, T _A =25°C	Fig.14
	6N136 MPC4502		1000	-	-		I _F = 0mA, V _{CM} =10Vpp, R _L =1.9kΩ, T _A =25°C	
	MPC4503		15000	20000	-		I _F = 0mA, V _{CM} =1500Vpp, R _L =1.9kΩ, T _A =25°C	
Common Mode Transient Immunity at Logic Low	6N135	CM _L	1000	-	-	V/μs	I _F = 16mA, V _{CM} =10Vpp, R _L =4.1kΩ, T _A =25°C	Fig.14
	6N136 MPC4502		1000	-	-		I _F = 16mA, V _{CM} =10Vpp, R _L =1.9kΩ, T _A =25°C	
	MPC4503		15000	20000	-		I _F = 16mA, V _{CM} =1500Vpp, R _L =1.9kΩ, T _A =25°C	

CHARACTERISTIC CURVES

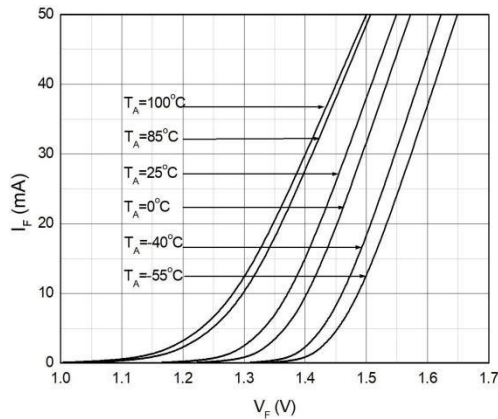


Fig.1 Forward Current vs. Forward Voltage

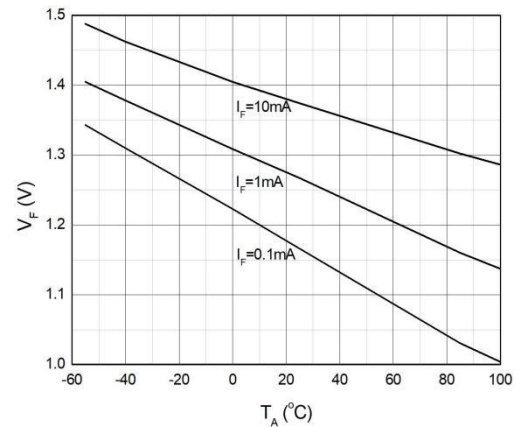


Fig.2 Forward Voltage vs. Ambient Temperature

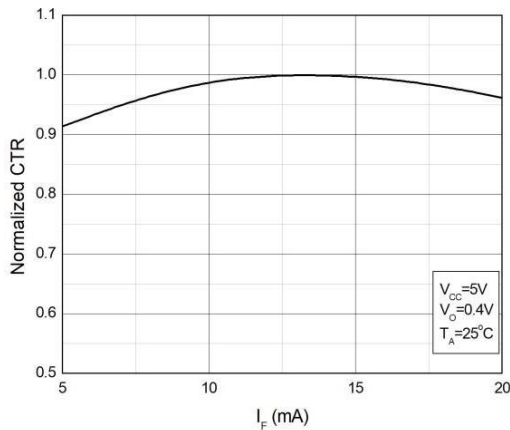


Fig.3 Input Threshold Current vs. Ambient Temperature

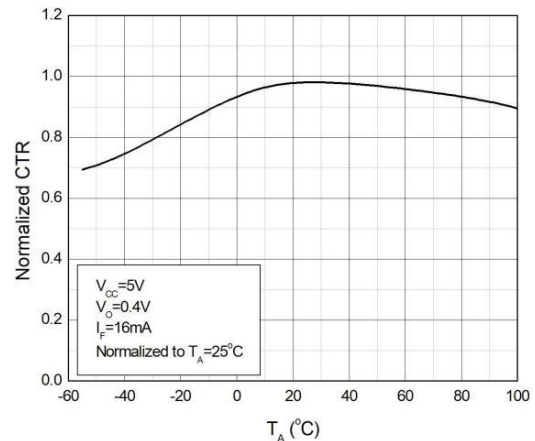


Fig.4 Input Threshold Current vs. Ambient Temperature

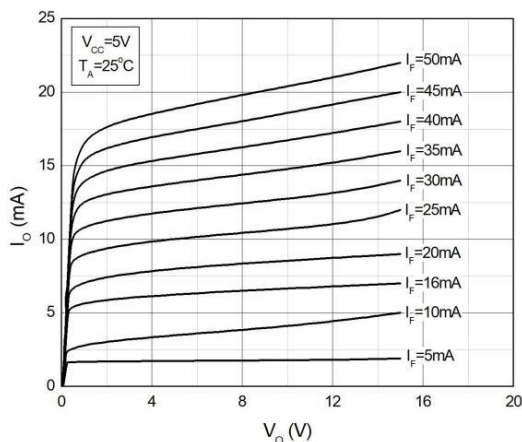


Fig.5 Low Level Output Current vs. Ambient Temperature

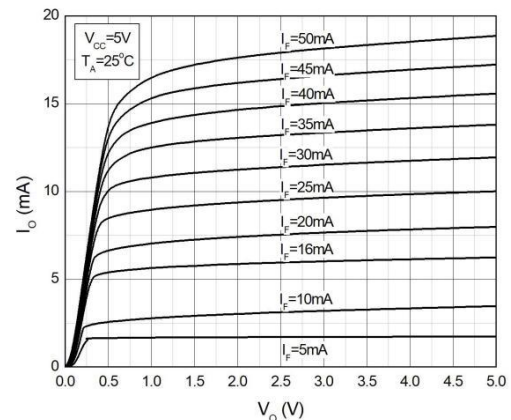


Fig.6 Low Level Output Current vs. Ambient Temperature

CHARACTERISTIC CURVES

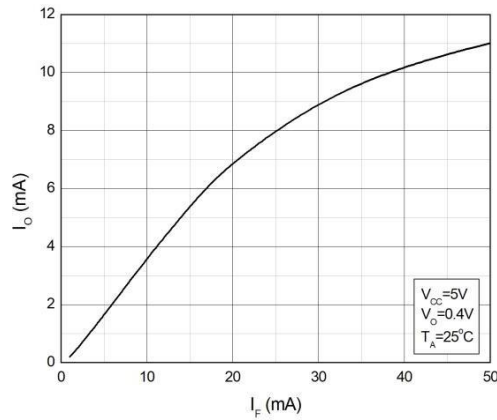


Fig.7 Low Level Output Voltage vs. Ambient Temperature

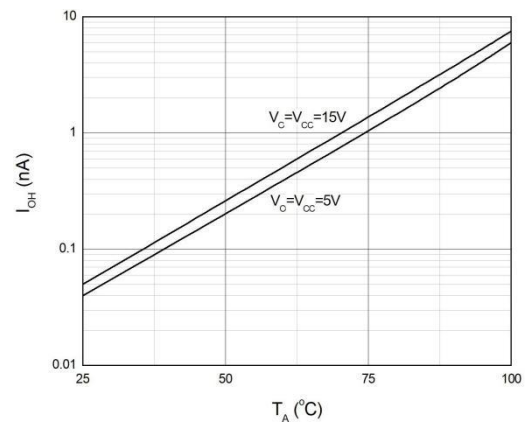


Fig.8 Low Level Output Voltage vs. Ambient Temperature

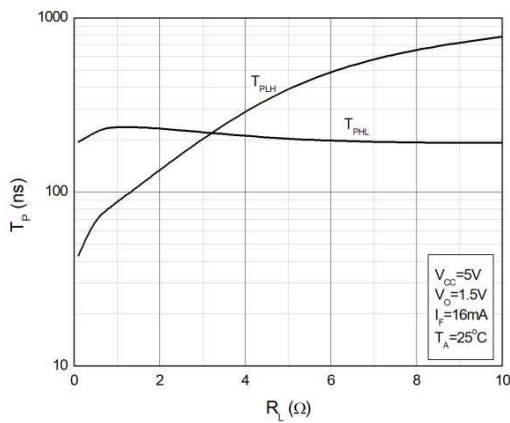


Fig.9 High Level Output Current vs. Ambient Temperature

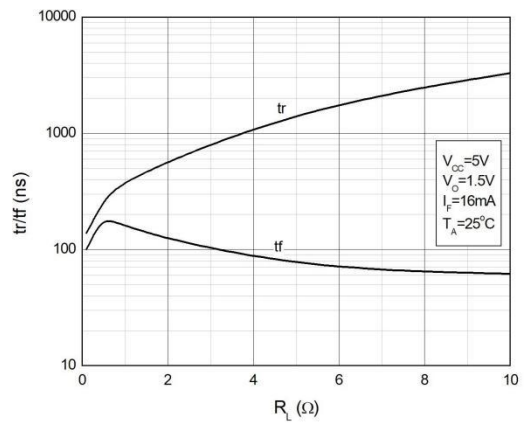


Fig.10 High Level Output Current vs. Ambient Temperature

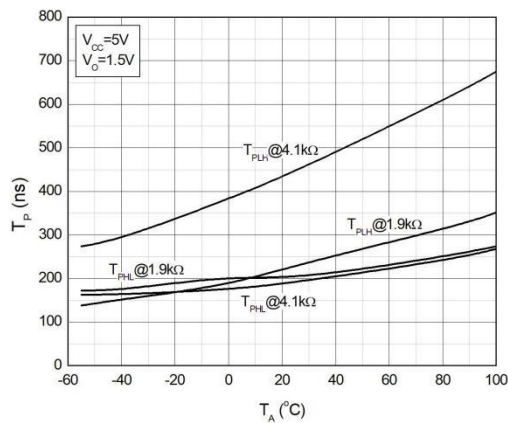


Fig.11 Output Voltage vs. Forward Current

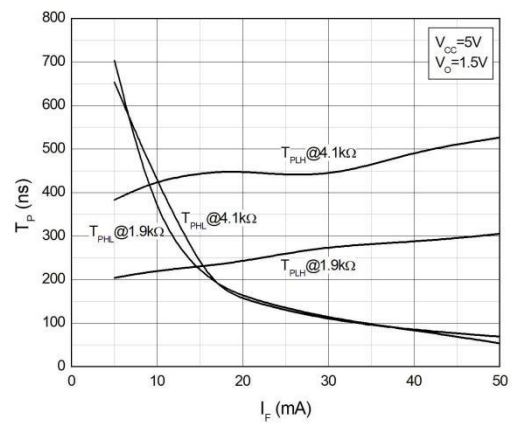


Fig.12 Output Voltage vs. Forward Current

TEST CIRCUITS

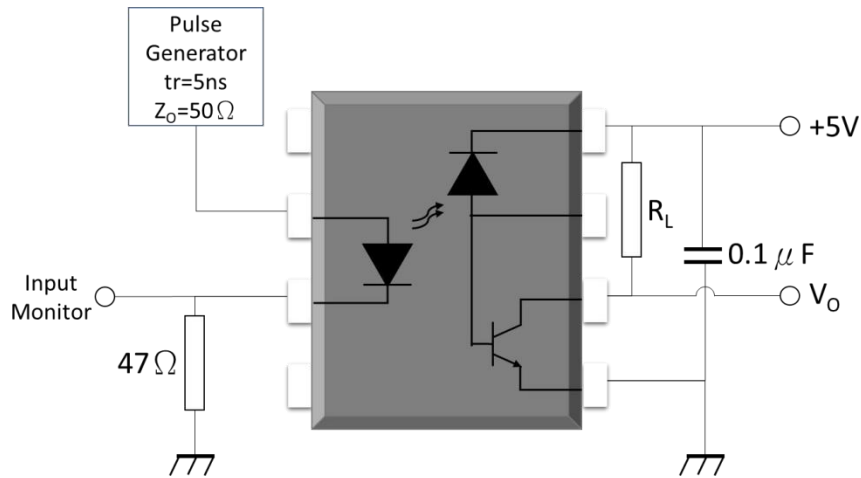


Fig.13 Test Circuits for TPHL, TPLH, t_r , t_f

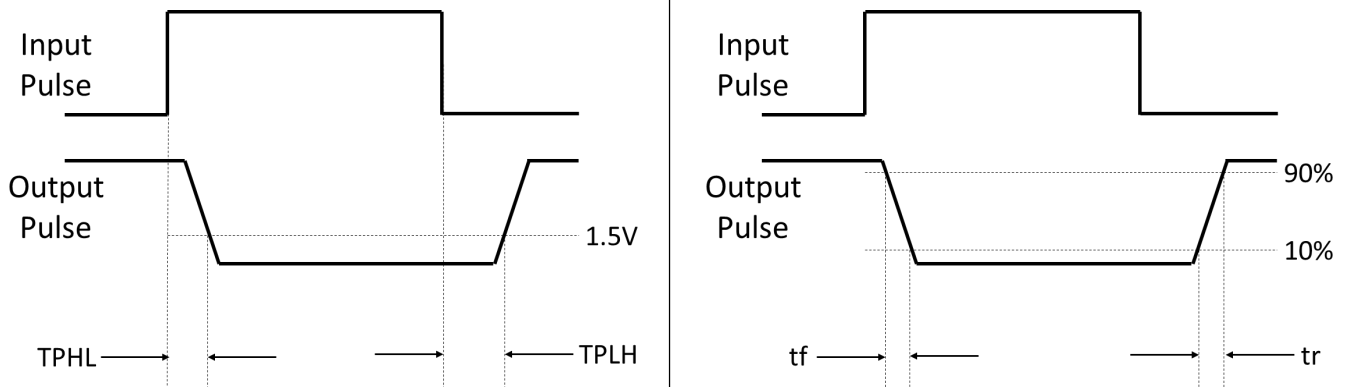


Fig.14 Waveforms of TPHL, TPLH, t_r , t_f

TEST CIRCUITS

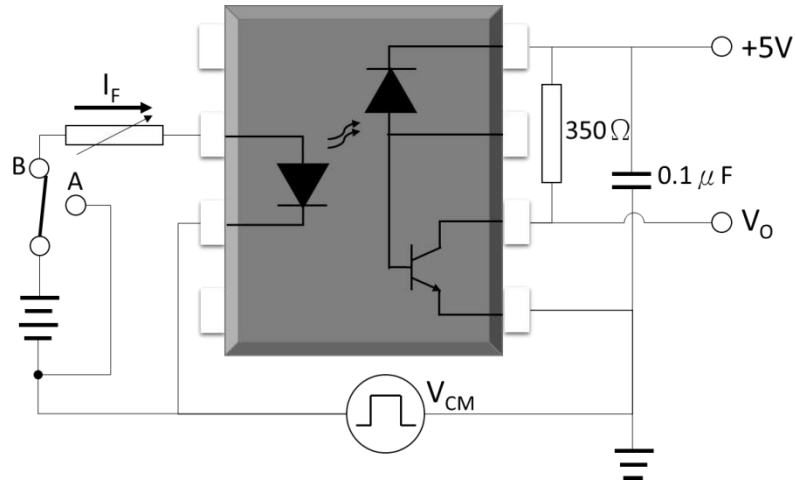


Fig.15 Test Circuits for Common Mode Transient Immunity

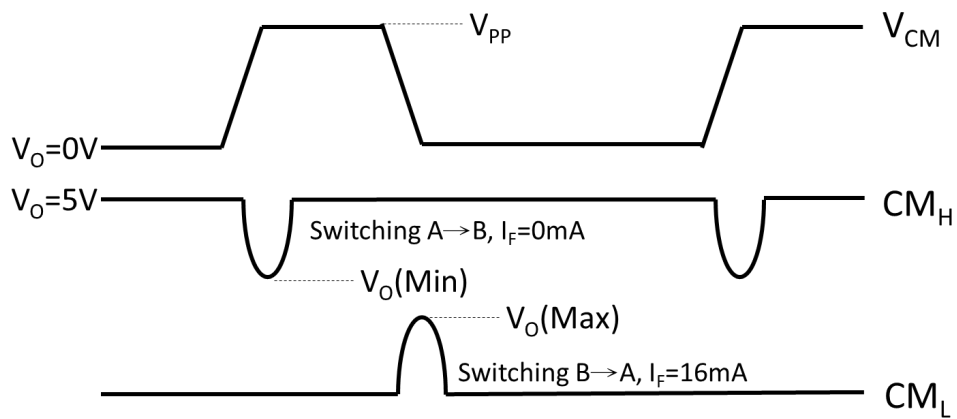
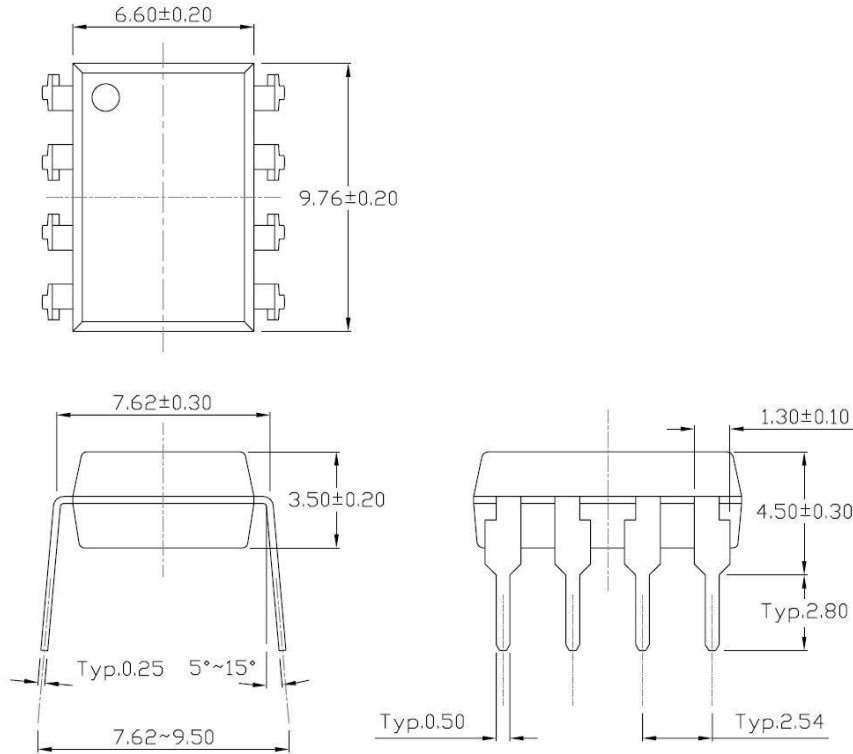


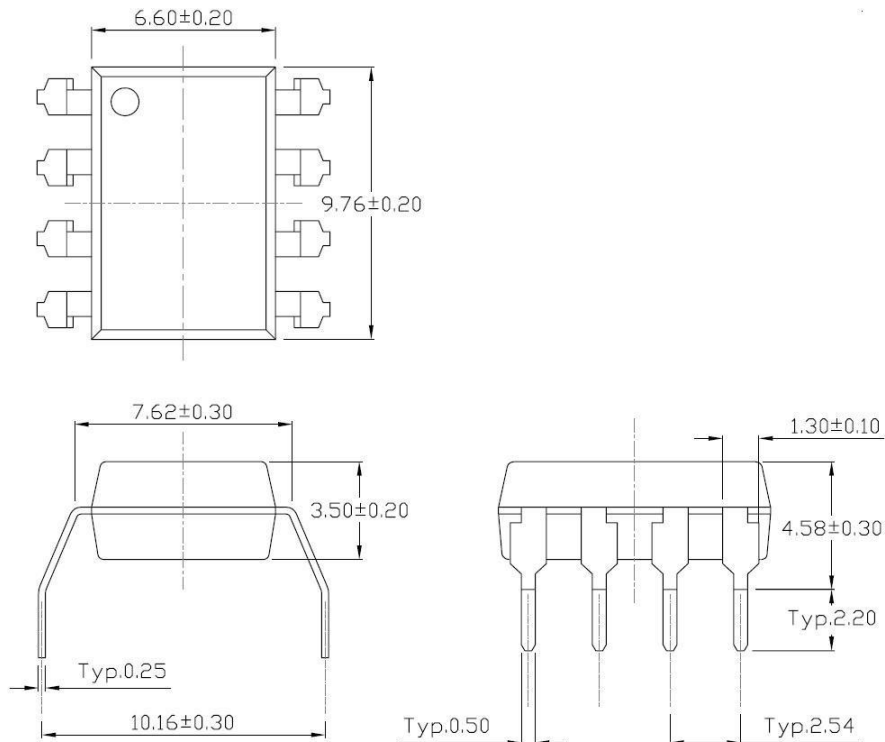
Fig.16 Waveforms of Common Mode Transient Immunity

PACKAGE DIMENSIONS (Dimensions in mm unless otherwise stated)

Standard DIP – Through Hole (DIP Type)

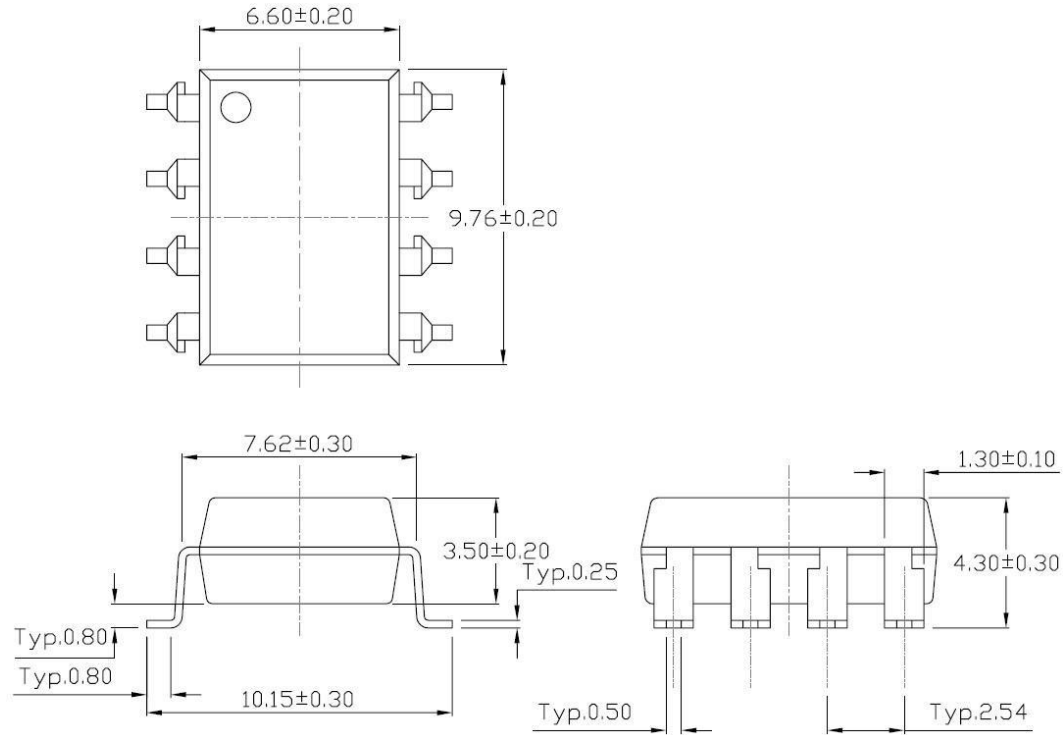


Gullwing (400mil) Lead Forming – Through Hole (M Type)

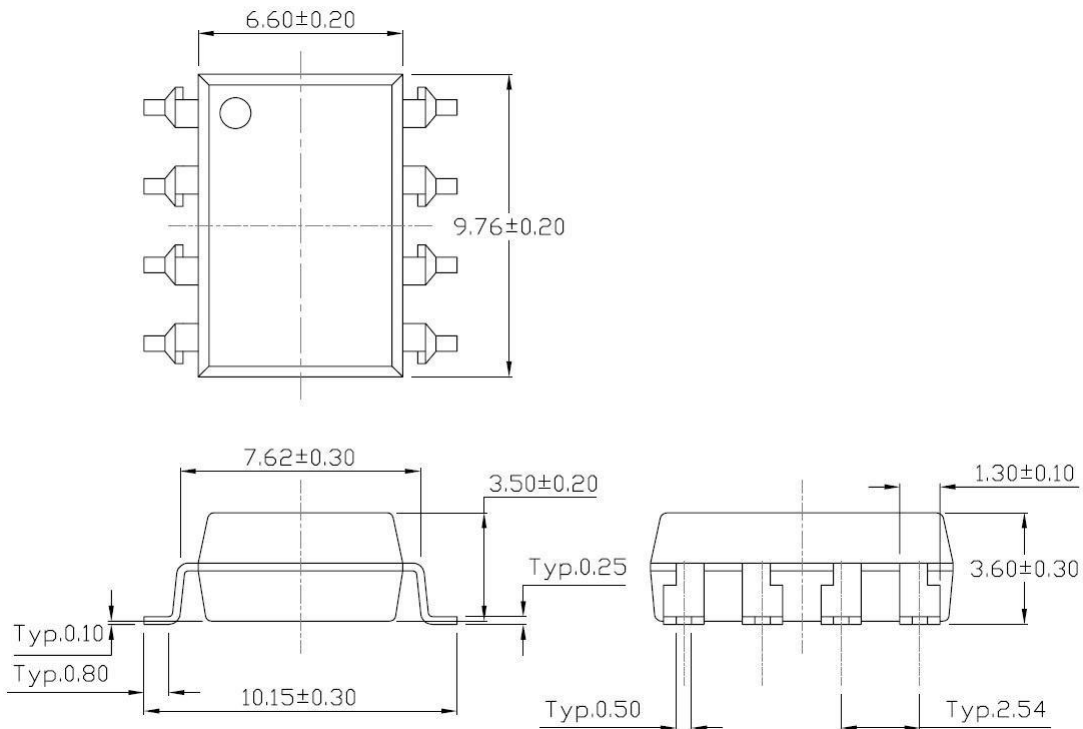


PACKAGE DIMENSIONS (Dimensions in mm unless otherwise stated)

Surface Mount Lead Forming (S Type)

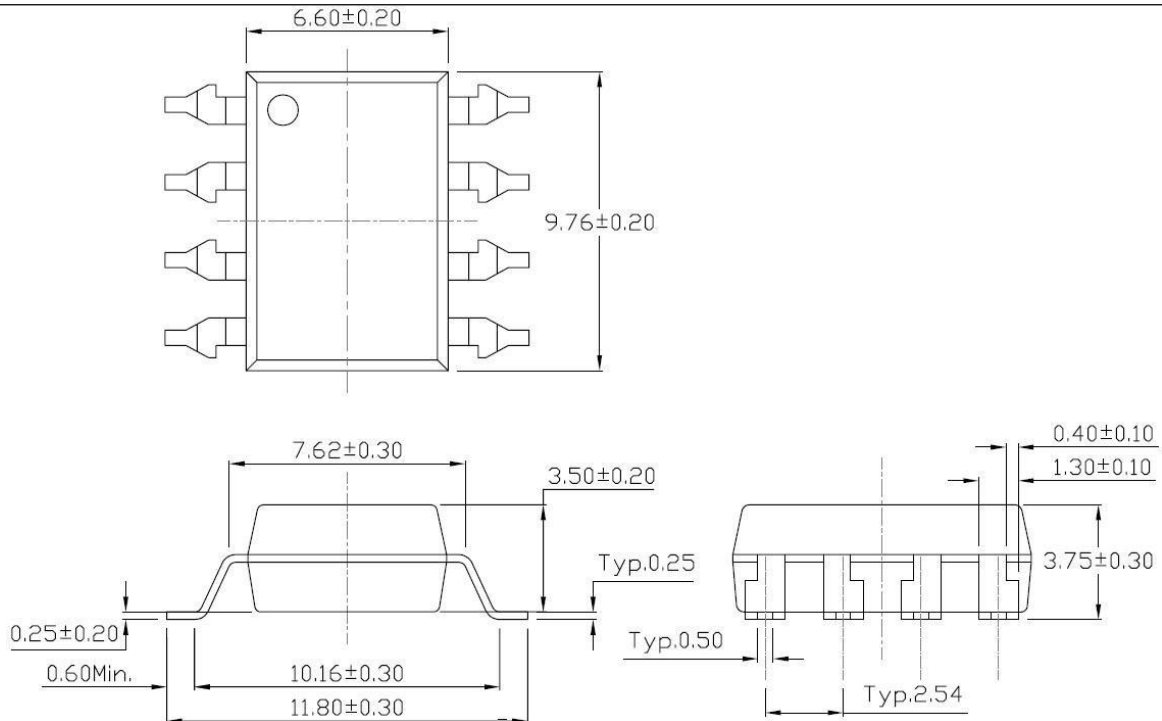


Surface Mount (Low Profile) Lead Forming (SL Type)



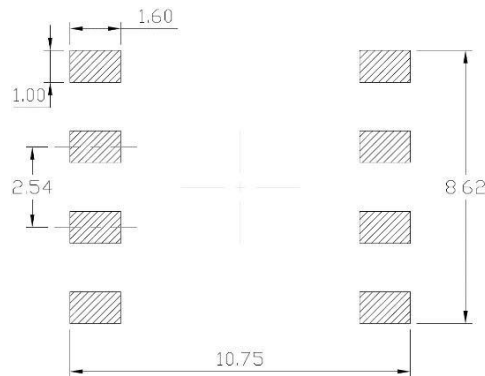
PACKAGE DIMENSIONS (Dimensions in mm unless otherwise stated)

Surface Mount (Gullwing) Lead Forming (SLM Type)

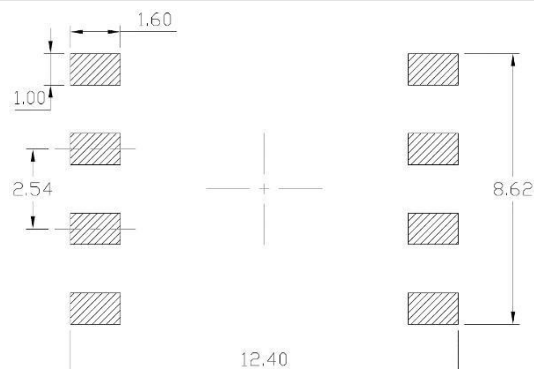


Recommended Solder Mask (Dimensions in mm unless otherwise stated)

Surface Mount Lead Forming & Surface Mount (Low Profile) Lead Forming

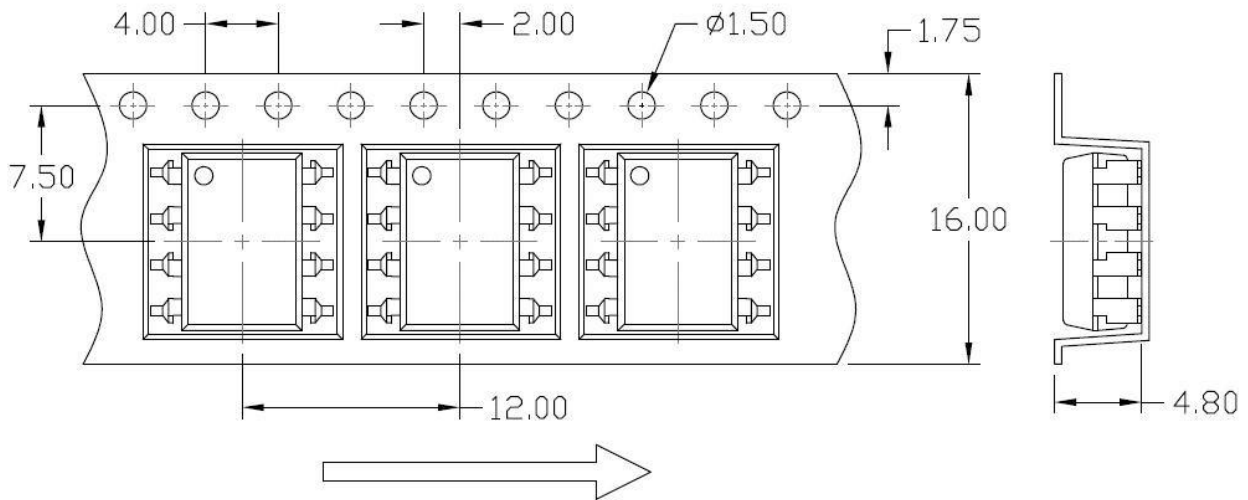


Surface Mount (Gullwing) Lead Forming

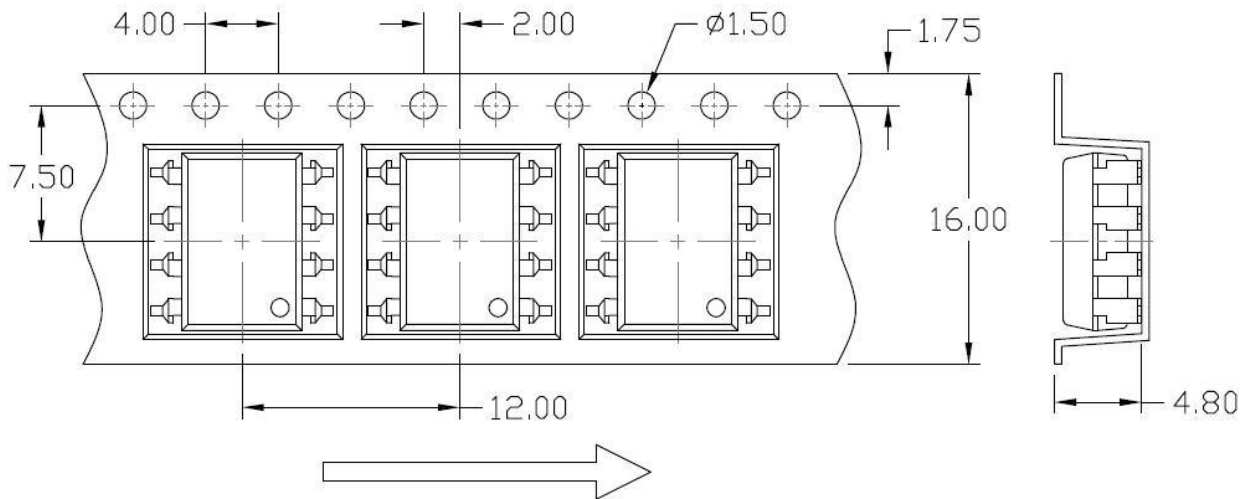


Carrier Tape Specifications (Dimensions in mm unless otherwise stated)

Option S(T1) & SL(T1)

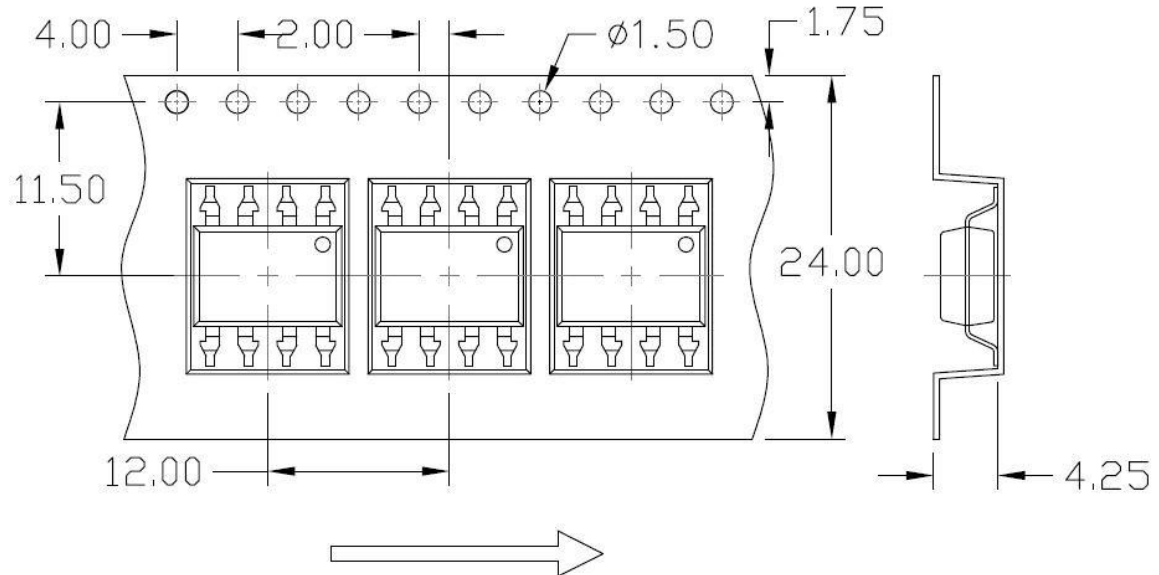


Option S(T2) & SL(T2)

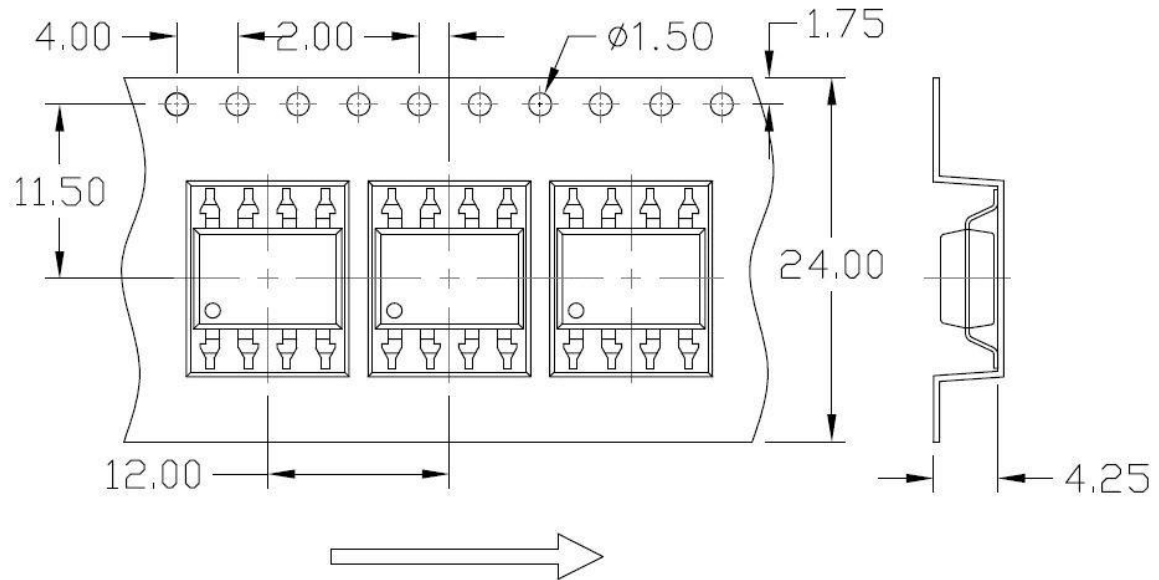


Carrier Tape Specifications (Dimensions in mm unless otherwise stated)

Option SLM(T1)

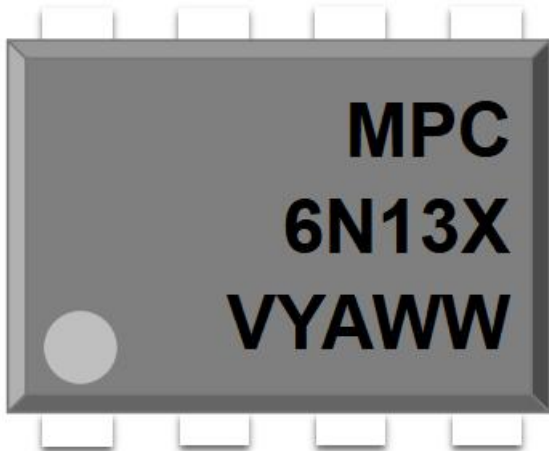


Option SLM(T2)



ORDERING AND MARKING INFORMATION

MARKING INFORMATION



MPC : Company Abbr.

6N13X : Part Number& Rank

V : VDE Option

Y : Fiscal Year

A : Manufacturing Code

WW : Work Week

ORDERING INFORMATION

6N13X(Y)(Z)-GV

6N13X – Part Number & Rank(X=5 or 6)

Y – Lead Form Option (M/S/SL/SLM/None)

Z – Tape and Reel Option (T1/T2)

G – Material Option (G: Green, None: Non-Green)

V – VDE Option (V or None)

PACKING QUANTITY

Option	Description	Quantity
None	Standard 8 Pin Dip	50Units/Reel
M	Gullwing(400mil) Lead Forming	50Units/Reel
S(T1)	Surface Mount Lead Forming – With Option 1 Taping	1000Units/Tube
S(T2)	Surface Mount Lead Forming – With Option 2 Taping	1000Units/Tube
SL(T1)	Surface Mount Lead Forming(Low Profile) – With Option 1 Taping	1000Units/Tube
SL(T2)	Surface Mount Lead Forming(Low Profile) – With Option 2 Taping	1000Units/Tube

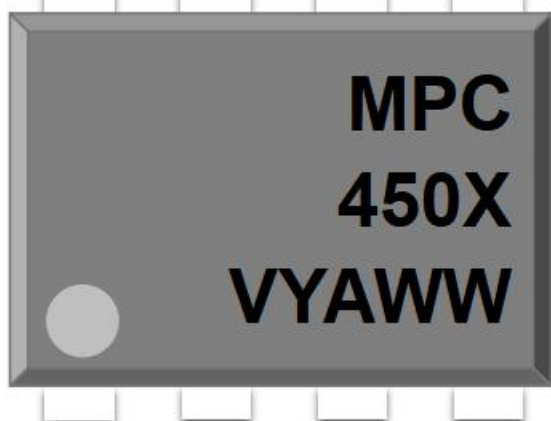


6N135,6N136,MPC4502,MPC4503 Series

DIP8, 1Mbit/s High Speed Transistor Photo Coupler

ORDERING AND MARKING INFORMATION

MARKING INFORMATION



MPC : Company Abbr.

450X : Part Number& Rank

V : VDE Option

Y : Fiscal Year

A : Manufacturing Code

WW : Work Week

ORDERING INFORMATION

MPC450X(Y)(Z)-GV

MPC – Company Abbr.

450X – Part Number&Rank (X=2 or 3)

Z – Tape and Reel Option (T1/T2)

G – Material Option (G: Green, None: Non-Green)

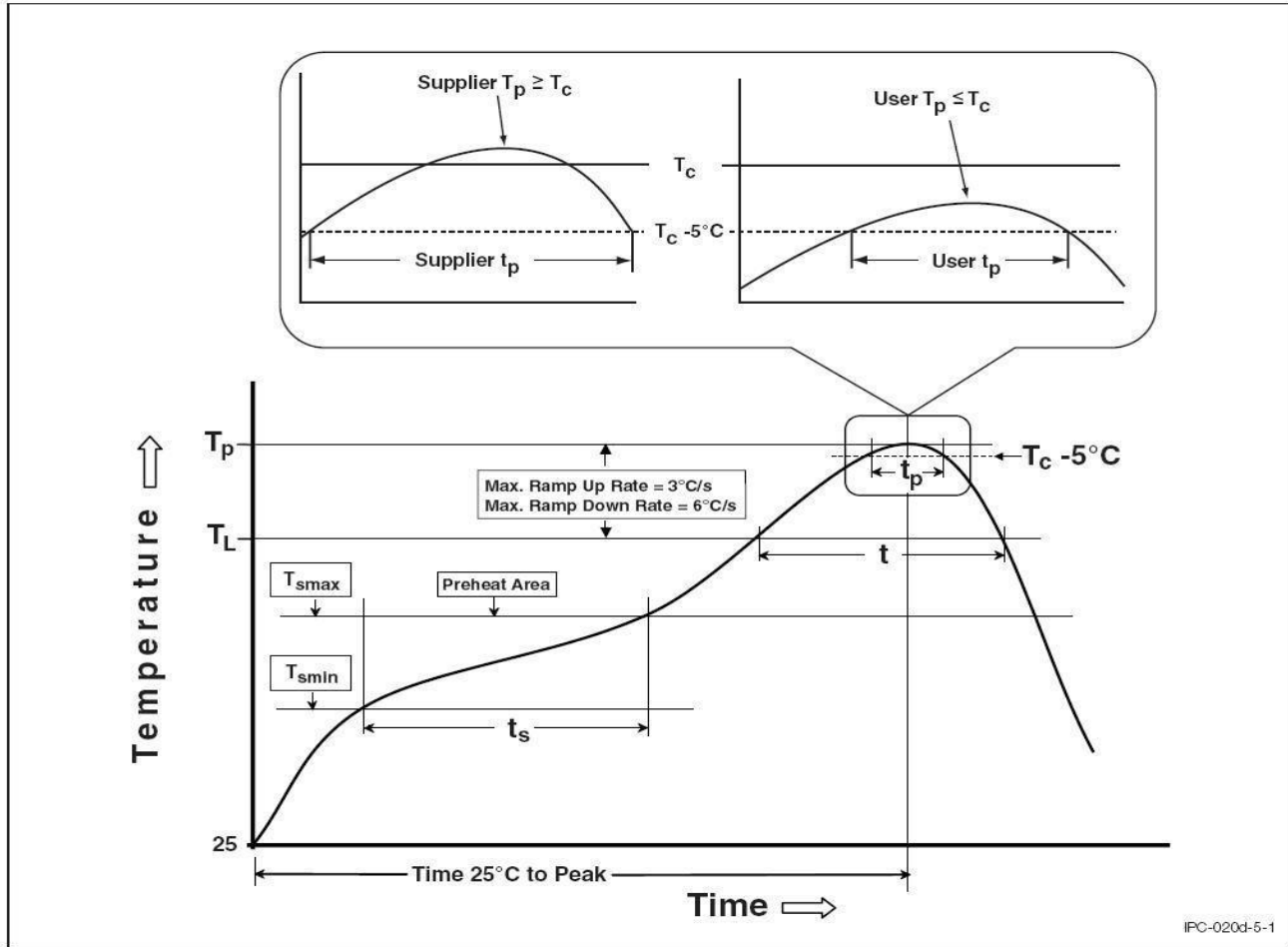
V – VDE Option (V or None)

PACKING QUANTITY

Option	Description	Quantity
None	Standard 8 Pin Dip	50Units/Reel
M	Gullwing(400mil) Lead Forming	50Units/Reel
S(T1)	Surface Mount Lead Forming – With Option 1 Taping	1000Units/Tube
S(T2)	Surface Mount Lead Forming – With Option 2 Taping	1000Units/Tube
SL(T1)	Surface Mount Lead Forming(Low Profile) – With Option 1 Taping	1000Units/Tube
SL(T2)	Surface Mount Lead Forming(Low Profile) – With Option 2 Taping	1000Units/Tube

REFLOW INFORMATION

REFLOW PROFILE



Profile Feature	Sn-Pb Assembly Profile	Pb-Free Assembly Profile
Temperature Min. (T _{smin})	100	150°C
Temperature Max. (T _{smax})	150	200°C
Time (t _s) from (T _{smin} to T _{smax})	60-120 seconds	60-120 seconds
Ramp-up Rate (t _L to t _P)	3°C/second max.	3°C/second max.
Liquidous Temperature (T _L)	183°C	217°C
Time (t _L) Maintained Above (T _L)	60 – 150 seconds	60 – 150 seconds
Peak Body Package Temperature	235°C +0°C / -5°C	260°C +0°C / -5°C
Time (t _P) within 5°C of 260°C	20 seconds	30 seconds
Ramp-down Rate (T _P to T _L)	6°C/second max	6°C/second max
Time 25°C to Peak Temperature	6 minutes max.	8 minutes max.



6N135,6N136,MPC4502,MPC4503 Series

DIP8, 1Mbit/s High Speed Transistor Photo Coupler

DISCLAIMER

- Our company is continually improving the quality, reliability, function and design. Our company reserves the right to make changes without further notices.
- The characteristic curves shown in this datasheet are representing typical performance which are not guaranteed.
- This product is not intended to be used for military, aircraft, automotive, medical, life sustaining or lifesaving applications or any other application which can result in human injury or death.
- Immerge unit's body in solder paste is not recommended.
- Discoloration might be occurred on the package surface after soldering, reflow or long-time use. It neither impacts the performance nor reliability.

■ Revision History

Version	Date	Subjects (major changes since last revision)
1.0	2022-07-21	Datasheet Complete
1.1	2023-07-05	Upgrade Datasheet