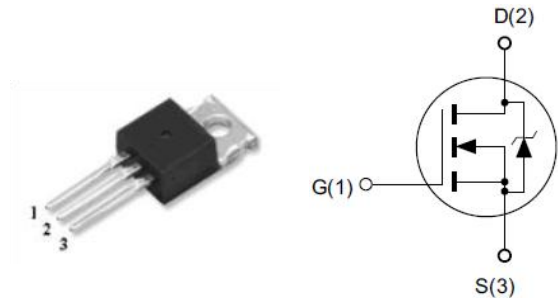


Features

- ◆ 85V, 120A, $R_{DS(ON)}$ (Typ.) = 4.6mΩ@ $V_{GS} = 10V$
- ◆ Ultra-low $R_{DS(ON)}$ and Low Gate Charge
- ◆ 100% EAS Guaranteed
- ◆ Halogen Free and RoHS-Compliant



Application

- ◆ Load Switching, Quick/Wireless Charging, Motor Driving
- ◆ Current Switching in DC/DC & AC/DC (SR) Sub Systems
- ◆ Power Management in Computing, Communications

Absolute Maximum Ratings $T_c = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Unit
V_{DS}	Drain-Source Voltage ^a	85	V
V_{GS}	Gate-Source Voltage	±20	
I_D	Drain Current-Continuous	$T_C = 25^\circ\text{C}$	A
		$T_C = 100^\circ\text{C}$	
I_{DM}	Drain Current-Pulsed ^b	480	
P_D	Maximum Power Dissipation @ $T_J = 25^\circ\text{C}$	173	W
E_{AS}	Single Pulsed Avalanche Energy ^c	361	mJ
T_J, T_{STG}	Operating and Store Temperature Range	150, -55 to 150	$^\circ\text{C}$

Thermal Characteristics

Symbol	Parameter	Value	Unit
$R_{\theta JC}$	Thermal Resistance, Junction to Case	0.72	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	45	

Electrical Characteristics $T_J = 25^\circ\text{C}$ unless otherwise noted

■ Off Characteristics

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{GS} = 0V, I_D = 250\mu A$	85	91	-	V
I_{DSS}	Zero Gate Voltage Drain Current	$V_{DS} = 80V, V_{GS} = 0V$	-	-	1	μA
I_{GSS}	Forward Gate Body Leakage Current	$V_{DS} = 0V, V_{GS} = \pm 20V$	-	-	±100	nA

■ On Characteristics

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
$V_{GS(th)}$	Gate Threshold Voltage	$V_{DS} = V_{GS}, I_D = 250\mu A$	2.0	-	4.0	V
$R_{DS(on)}$	Static Drain-Source On-Resistance ^d	$V_{GS} = 10V, I_D = 20A$	-	4.6	5.5	m Ω

■ Dynamic Characteristics

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
R_g	Gate Resistance	$V_{DS} = 0V, V_{GS} = 0V, f = 1MHz$	-	1.5	-	Ω
C_{iss}	Input Capacitance	$V_{DS} = 40V, V_{GS} = 0V, f = 1MHz$	-	3503	-	pF
C_{oss}	Output Capacitance		-	1048	-	
C_{rss}	Reverse Transfer Capacitance		-	23	-	

■ On Characteristics

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
$t_{d(on)}$	Turn-On Delay Time	$V_{DD} = 40V, V_{GS} = 10V, R_L = 2\Omega, R_{GEN} = 3\Omega$	-	15	-	ns
t_r	Turn-On Rise Time		-	23	-	
$t_{d(off)}$	Turn-Off Delay Time		-	31	-	
t_f	Turn-Off Fall Time		-	13	-	
Q_g	Total Gate Charge	$V_{DD} = 40V, V_{GS} = 0 \text{ to } 10V, I_D = 20A$	-	51	-	nC
Q_{gs}	Gate-Source Charge		-	20	-	
Q_{gd}	Gate-Drain Charge		-	10	-	

■ Drain-Source Diode Characteristics

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Unit
I_S	Drain-Source Diode Forward Continuous Current	-	-	-	120	A
I_{SM}	Maximum Pulsed Current	-	-	-	480	
V_{SD}	Drain-Source Diode Forward Voltage	$V_{GS} = 0V, I_S = 1A$	-	0.7	1	V
T_{rr}	Body Diode Reverse Recovery Time	$I_F = 15A, di_F/dt = 100A/\mu s$	-	57	-	ns
Q_{rr}	Body Diode Reverse Recovery Charge	$I_F = 15A, di_F/dt = 100A/\mu s$	-	91	-	nC

Notes:

- $T_J = +25^\circ C$ to $+150^\circ C$
- Repetitive rating; pulse width limited by maximum junction temperature.
- $L = 0.5mH, V_{DD} = 50V, I_{AS} = 38A, R_G = 25\Omega$ Starting $T_J = 25^\circ C$.
- Pulse width $\leq 300\mu s$; duty cycle $\leq 2\%$.

■ Characteristic Curve

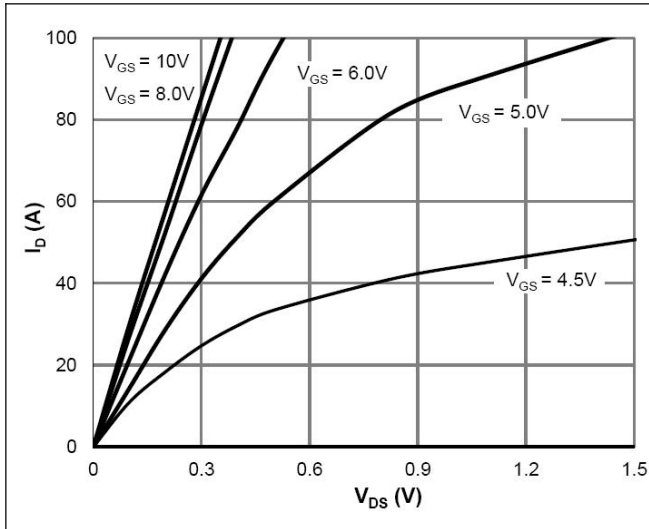


Figure 1. Typical Output Characteristics

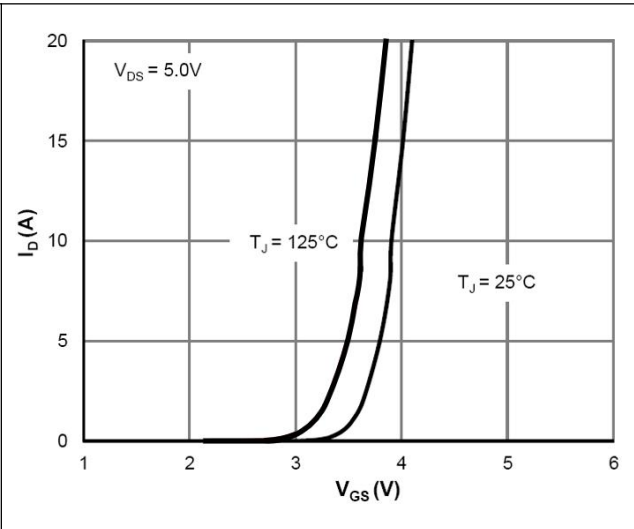


Figure 2. Typical Transfer Characteristics

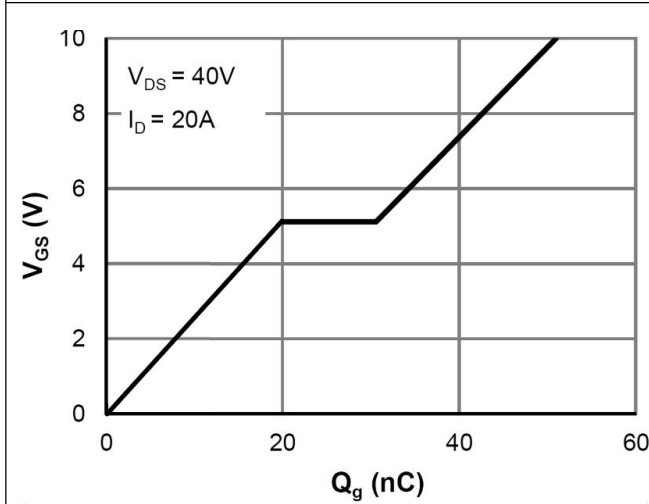


Figure 3. Typical Gate Charge

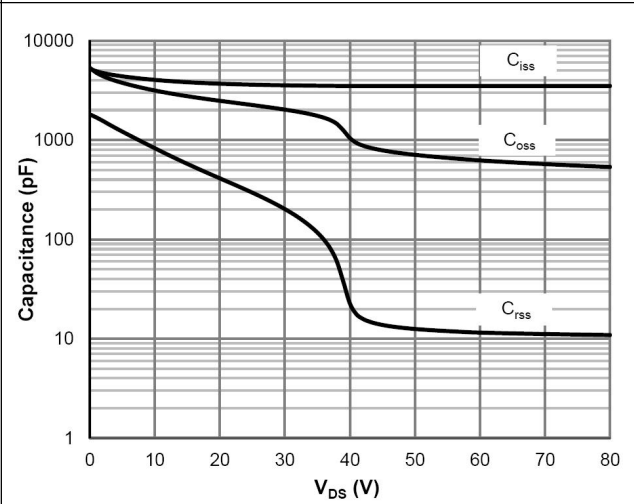


Figure 4. Typical Capacitance

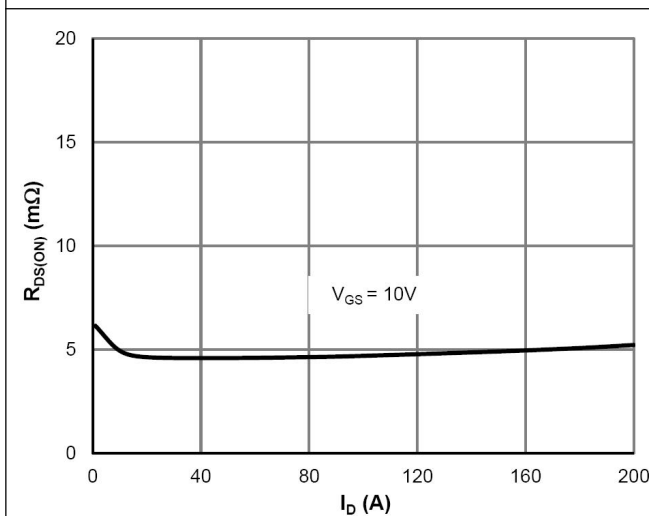


Figure 5. On-Resistance vs Drain Current

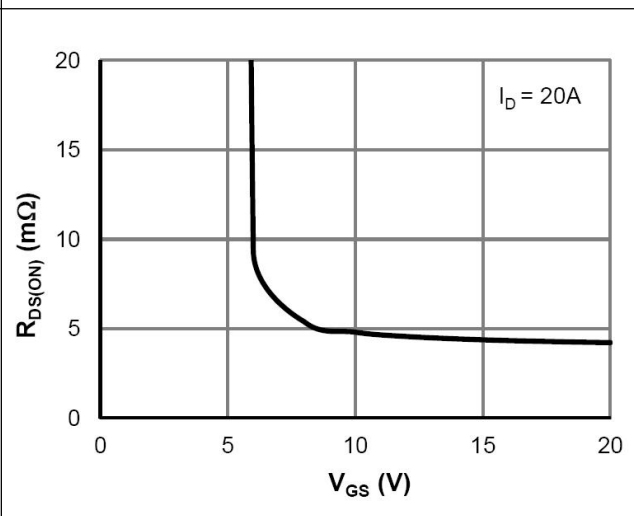


Figure 6. On-Resistance vs Gate-Source Voltage

■ Characteristic Curve

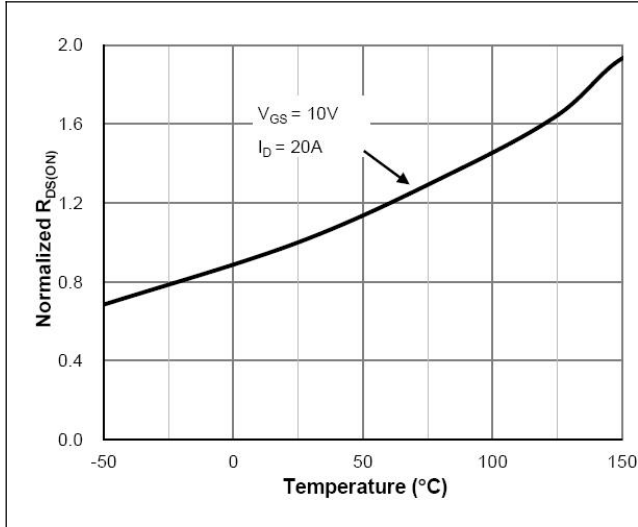


Figure 7. Normalized On-Resistance vs Junction Temperature

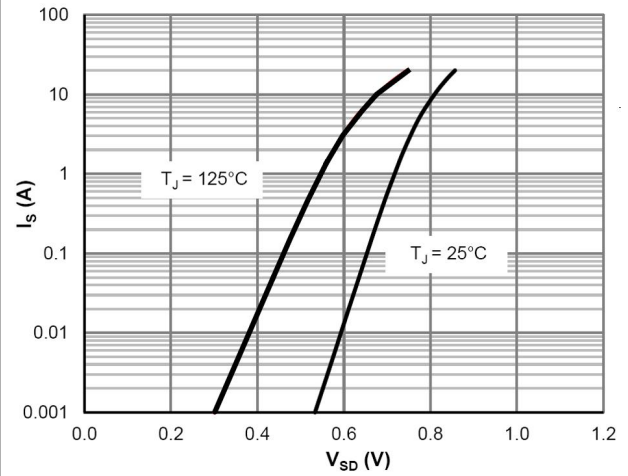


Figure 8. Body Diode Characteristics

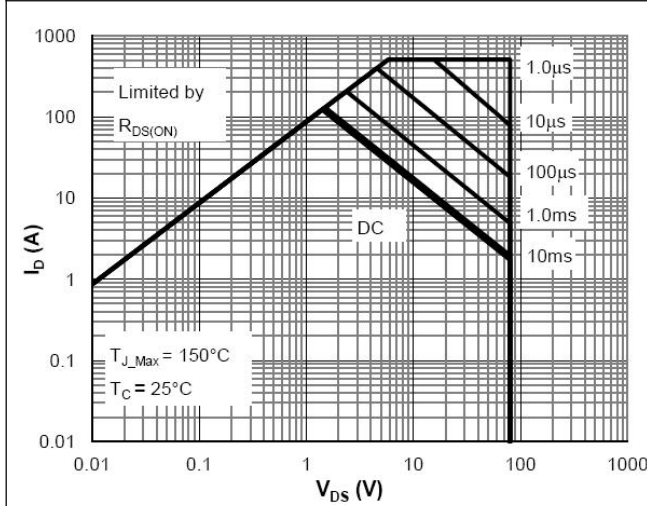


Figure 9. Maximum Safe Operating Area

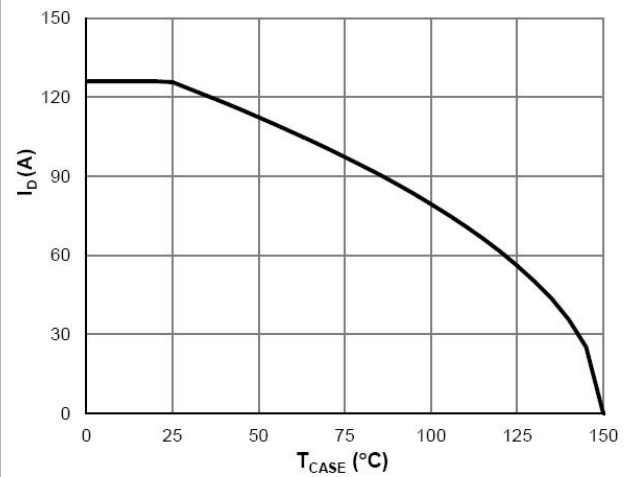


Figure 10. Maximum Continuous Drain Current vs Case Temperature

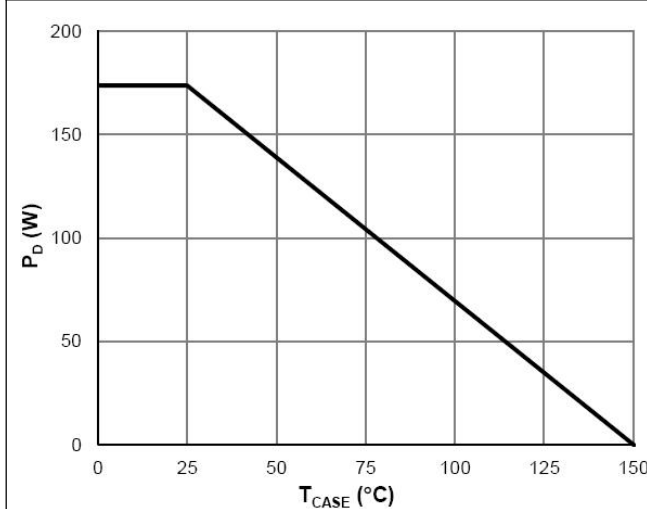


Figure 11. Maximum Power Dissipation vs Case Temperature

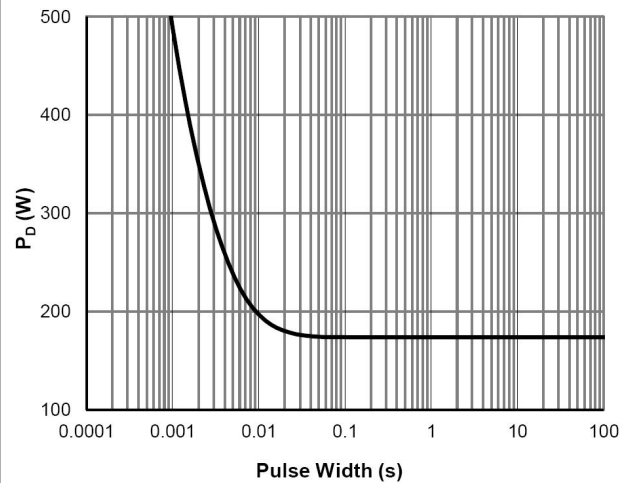


Figure 12. Single Pulse Power Rating, Junction-to-Case

■ Characteristic Curve

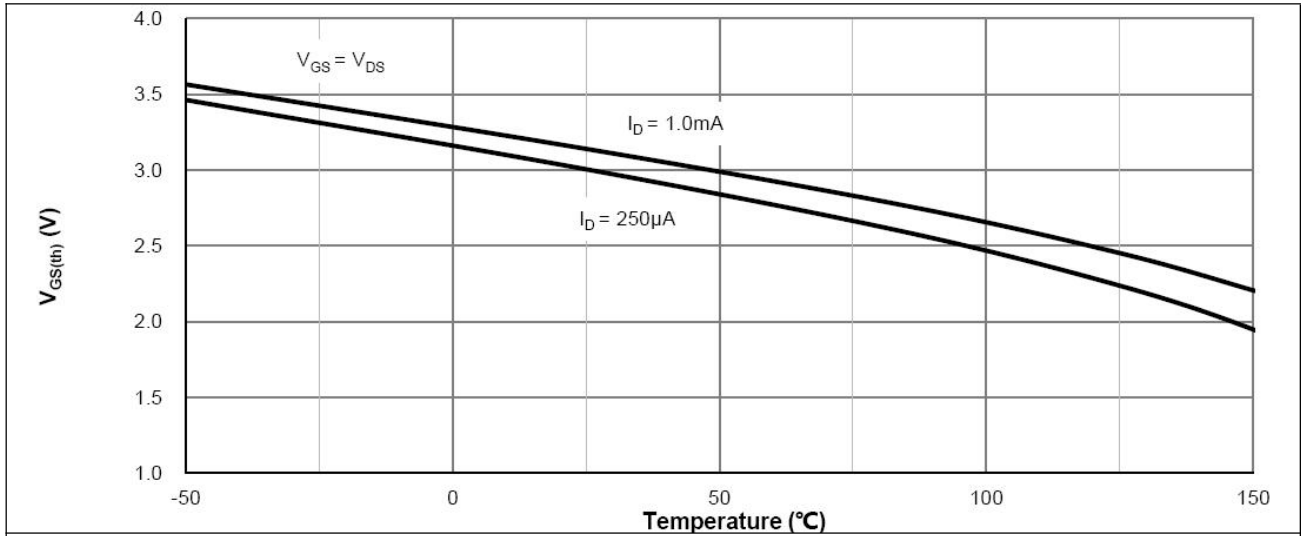


Figure 13. Normalized Threshold Voltage vs Junction Temperature

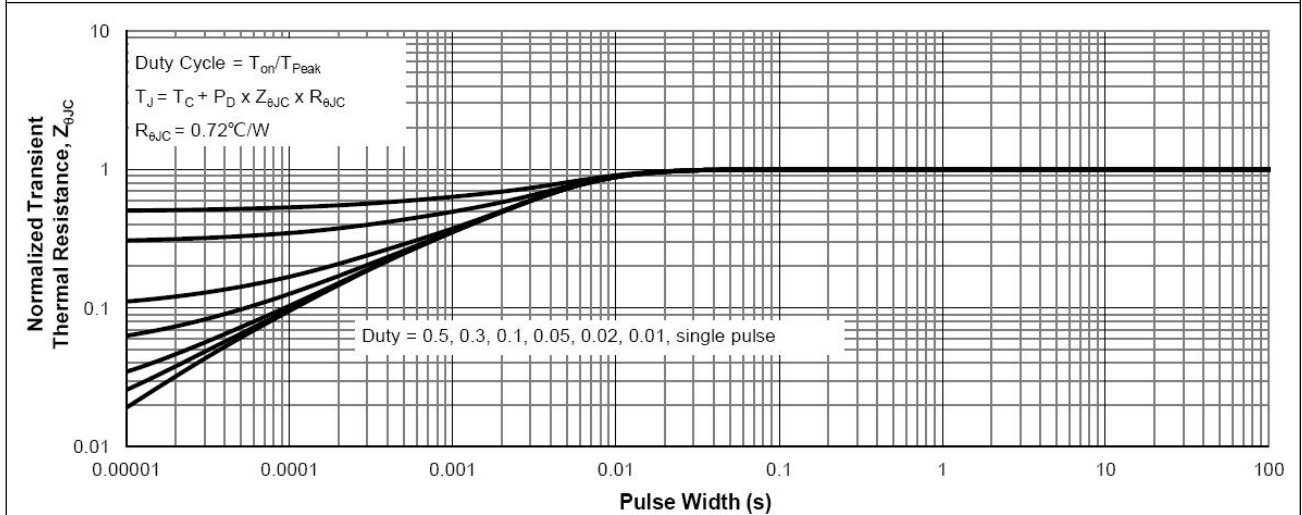
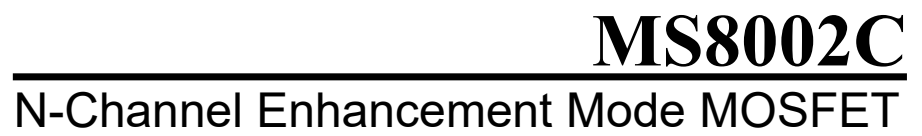


Figure 14. Transient Thermal Impedance



TO-220

