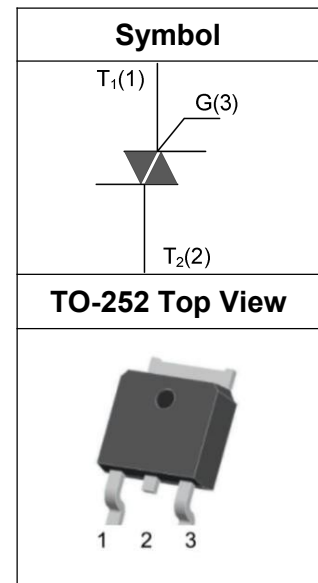


Features

- ◆ Five-layer NPNPN-Structured Silicon Bidirectional Device
- ◆ Single-Side Trenching Technology and Mesa Glass Passivation Process
- ◆ Backside Multilayer Metallized Electrode
- ◆ High Blocking Voltage and Superior Temperature Stability

Application

- ◆ Solid State Relay
- ◆ Heating Controller (Temperature Adjustment)
- ◆ Motor Speed Controllers for Vacuum Cleaners, Power Tools, and Similar Applications



Product Summary:

Symbol	Value	Unit
I _{T(RMS)}	2.0	A
V _{DRM}	800	V
V _{RRM}	800	

■ Absolute Maximum Ratings

Symbol	Parameter	Test Condition	Value	Unit
I _{T(RMS)}	RMS On-State Current	T _C = 90°C	2.0	A
I _{TSM}	Non-Repetitive Surge Peak On-State Current	F = 50Hz, t = 20ms	20	
I ² t	I ² t Value for Fusing	t _p = 10ms	3.0	A ² s
d _i /d _t	Critical Rate of Rise of On-State Current	T _J = 125°C	50	A/μs
V _{DRM}	Repetitive Peak Off-State Voltage	T _J = 25°C	800	V
V _{RRM}	Repetitive Peak Reverse Voltage	T _J = 25°C	800	
I _{GM}	Peak Gate Current	t _p = 20μs, T _J = 125°C	4.0	A
P _{G(AV)}	Average Gate Power Dissipation	T _J = 125°C	0.5	W
T _{stg}	Storage Junction Temperature Range	-	-40 to 150	°C
T _J	Operating Junction Temperature Range	-	-40 to 125	



BT02D80X(W)

2A Bidirectional Thyristor

Thermal Characteristics

Symbol	Parameter	Test Condition	Value(Max)	Unit
R _{th(j-c)}	Junction to Case (AC)	Insulated	2.05	°C/W
		Non-Insulated	1.25	

Electrical Characteristics (3 Quadrants)

Symbol	Parameter	Test Condition	Quadrant	Value		Unit
I _{GT}	Gate Trigger Current	V _D = 12V, R _L = 100Ω	I-II-III	Max	15	mA
V _{GT}	Gate Trigger Voltage				1.5	
V _{GD}	Gate Non-Trigger Voltage	T _J = 125°C		Min	0.2	
I _H	Holding Current	I _T = 0.5A		Max	60	mA
I _L	Latching Current	I _G = 1.2I _{GT}	I-III		60	
			II		100	
d _V /d _t	Critical Rate-of-Rise of Off-Stage Voltage	V _D = 2/3V _{DRM} , T _J = 125°C		Min	500	V/μs
(d _V /d _t) _c	Critical Rate-of-Rise of Commutation Voltage	T _J = 125°C			10	

Electrical Characteristics (4 Quadrants)

Symbol	Parameter	Test Condition	Quadrant	Value		Unit
I _{GT}	Gate Trigger Current	V _D = 12V, R _L = 100Ω	I-II-III	Max	15	mA
			IV		35	
V _{GT}	Gate Trigger Voltage		T _J = 125°C		I-II-III-IV	Min
V _{GD}	Gate Non-Trigger Voltage	0.2				
I _H	Holding Current	I _T = 0.5A		Max	30	mA
I _L	Latching Current	I _G = 1.2I _{GT}	I-III-IV		25	
			II		50	
d _V /d _t	Critical Rate-of-Rise of Off-Stage Voltage	V _D = 2/3V _{DRM} , T _J = 125°C		Min	500	V/μs
(d _V /d _t) _c	Critical Rate-of-Rise of Commutation Voltage	T _J = 125°C			10	

Static Characteristics

Symbol	Parameter	Test Condition	Value(Max)	Unit
V _{TM}	Peak ON-State Voltage	I _{TM} = 4.0A, T _J = 25°C	1.5	V
V _{TO}	Threshold Voltage	T _J = 125°C	0.92	
R _D	Dynamic Resistance	T _J = 125°C	36.8	mΩ
I _{DRM}	Repetitive Peak Off-State Current	V _D = V _{DRM} , V _R = V _R RM, T _J = 25°C	5.0	μA
I _R RM	Repetitive Peak Reverse Current	V _D = V _{DRM} , V _R = V _R RM, T _J = 125°C	1.0	mA

■ Characteristic Curve

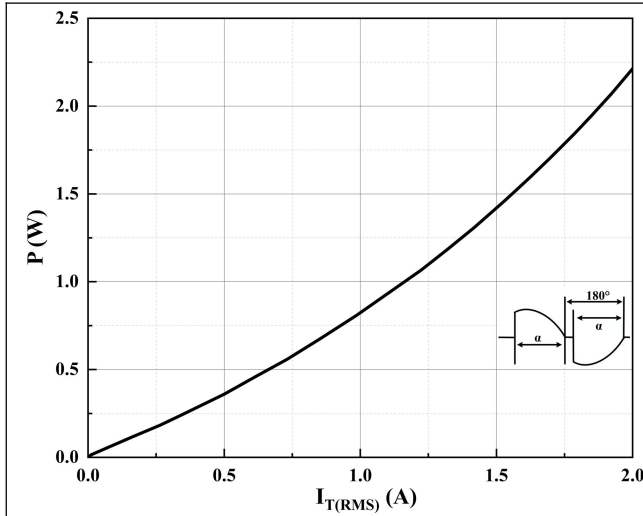


Figure 1. Power Dissipation vs. RMS On-State Current

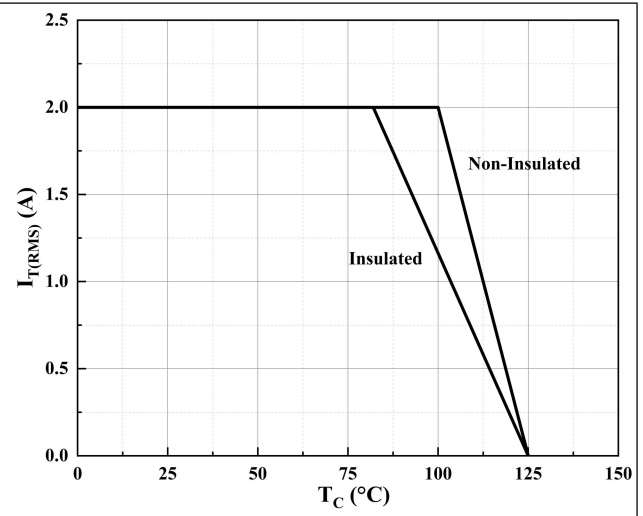


Figure 2. RMS On-State Current vs. Case Temperature

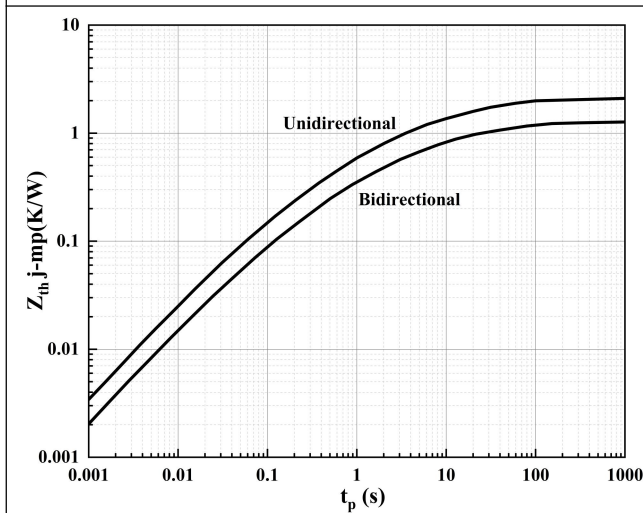


Figure 3. Relative Variation of Thermal Impedance Junction to Case vs. Pulse Duration

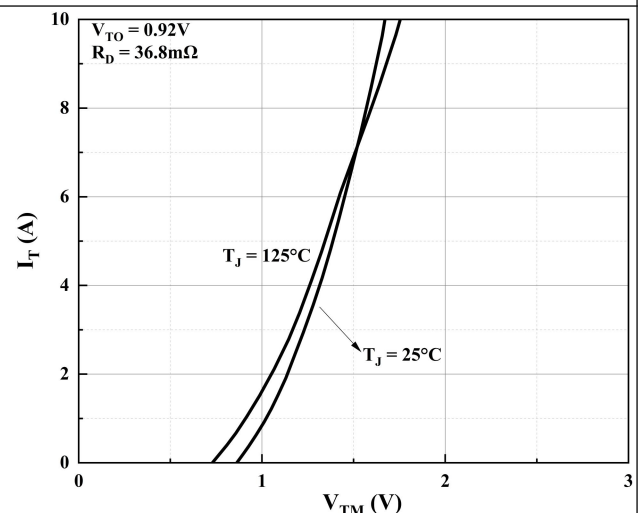


Figure 4. On-State Current vs. Peak ON-State Voltage

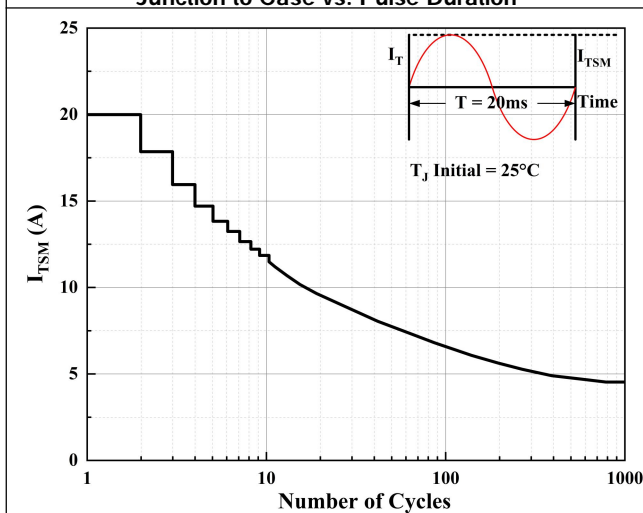


Figure 5. Surge Peak On-State Current vs. Number of Cycles

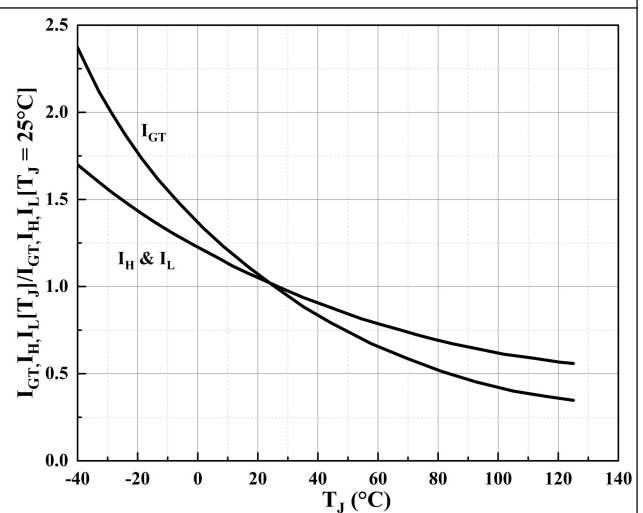


Figure 6. Gate Trigger Current, Holding Current and Latching Current vs. Junction Temperature

■ Characteristic Curve

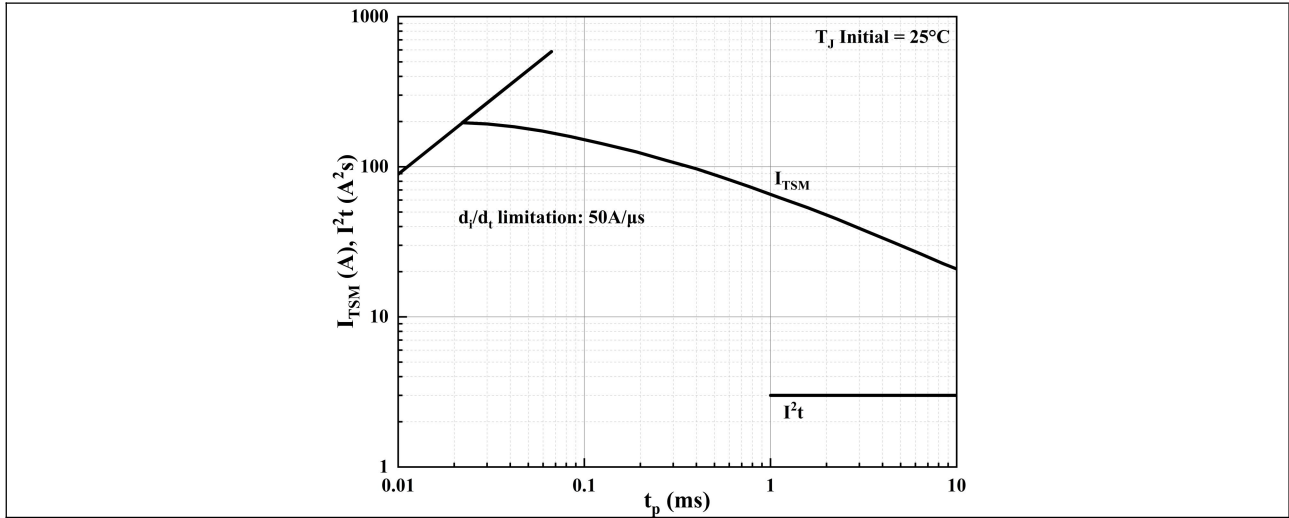
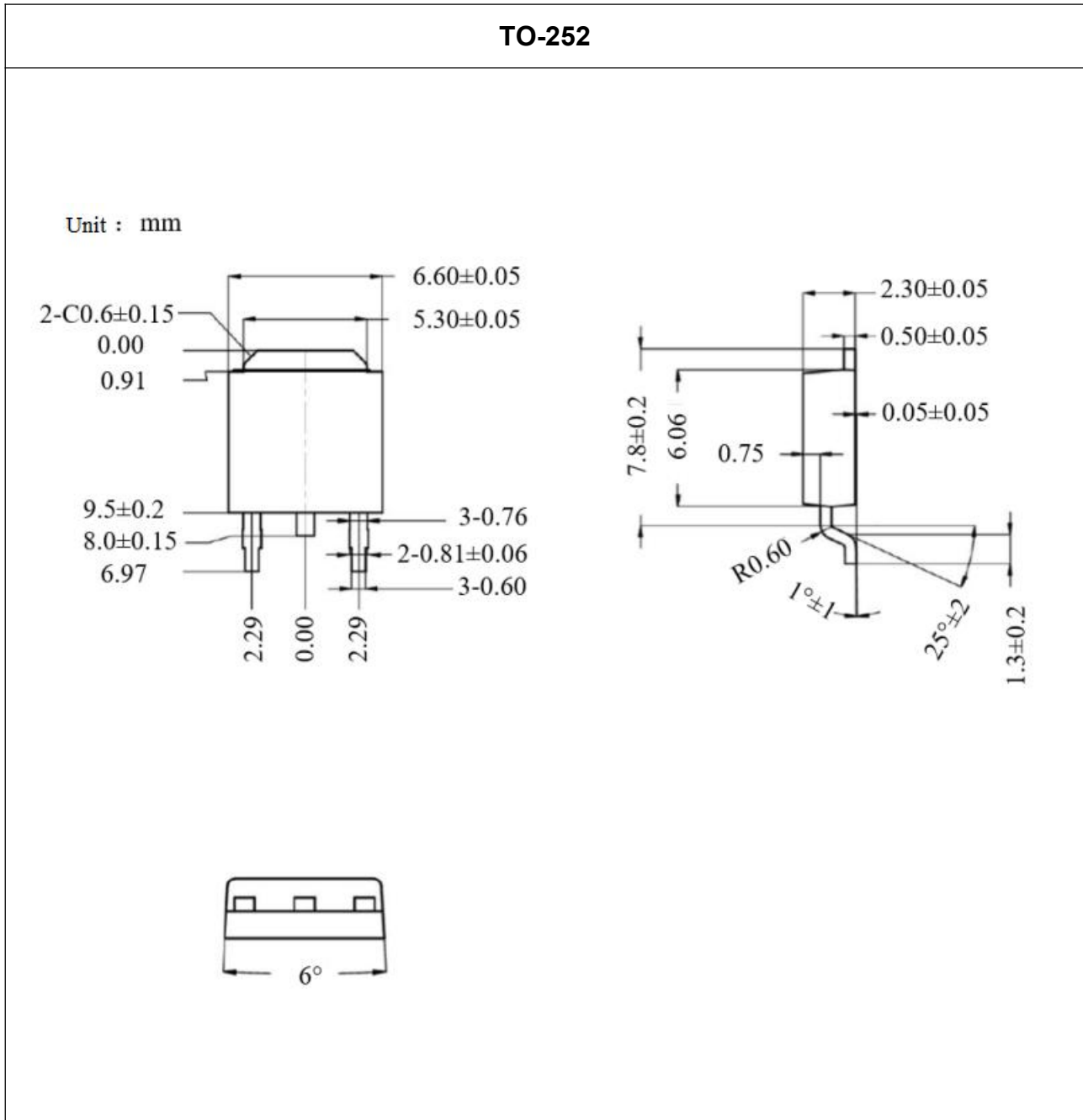


Figure 7. Non-Repetitive Surge Peak On-State Current for a Sinusoidal Pulse with Width $t_p < 10ms$ and Corresponding Value of I^2t ($d_i/d_t < 50A/\mu s$)

■ Ordering Information

BT	(A)	02	D	80	X	(W)	(H)
Triacs	A: Insulated None: Non-insulated	$I_{T(RMS)}: 2A$	TO-252	$V_{DRM} \geq 800V$ $V_{RRM} \geq 800V$		E: $I_{GT1-3} \leq 10mA$ C: $I_{GT1-3} \leq 15mA$ E: $I_{GT1-3} \leq 10mA$, $I_{GT4} \leq 25mA$ C: $I_{GT1-3} \leq 15mA$, $I_{GT4} \leq 35mA$	None: $T_J = 125^\circ C$ H: $T_J = 150^\circ C$
						W: 3 Quadrants None: 4 Quadrants	

■ Package Information



■ Revision History

Version	Date	Subjects (major changes since last revision)
1.0	2025-05-08	Datasheet Complete
1.1	2026-09-16	Add Ordering Information